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Document By Prem Pahlajrai**

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Faculty profile - marilyn c wolf

Marilyn C Wolf. Professor; VLSI Systems and Digital Design Phone: 404.894.5933 Dr. Wolf joined Georgia Tech as the Rhesa "Ray" S. Farmer,

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There are eleven Georgia Tech VLSI designs in the AHAT Program. Each of these designs has been produced by Georgia Tech using the Genesil Silicon Compiler. Each

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Faculty profile - abhijit chatterjee

Abhijit Chatterjee is a professor in the School and he served as chair of the VLSI Technical Interest Group at Georgia Tech from IEEE VLSI Design Symp

Richard demillo | georgia tech - college of

He returned to academia in 2002 as the John P. Imlay Jr. Dean of Computing at Georgia Tech, at the Georgia Institute of Technology in VLSI design. Rich has

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